

Formal Specification and Verification

Formal specification (3)

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Exam

- 1) 20.02.2017–24.02.2017 (first week after end of lectures)
27.02.2017–3.03.2017 (second week after end of lectures)
(Attention: 27.02: Rosenmontag)
6.03.2017–10.03.2017 (third week after end of lectures)
- 2) Before start of the lectures of the Sommersemester (18.04.2017)

Doodle poll soon.

Until now

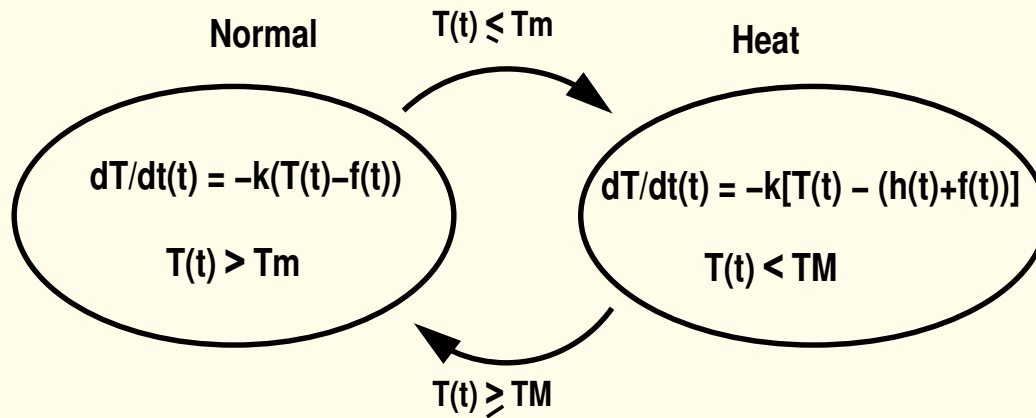
Transition systems and program graphs

Generalizations of transition systems

- More detailed description of states: Abstract state machines
- Emphasis on processes and their interdependency: CSP
- Durations: Timed automata
- Continuous evolution + discrete control: Hybrid automata

Hybrid Automata

Hybrid Automata



$f : \mathbb{R} \rightarrow \mathbb{R}$ evolution of external temperature

$h : \mathbb{R} \rightarrow \mathbb{R}$ evolution of heater temperature

Hybrid Automata

Hybrid automaton (HA) $S = (X, Q, \text{flow}, \text{Inv}, \text{Init}, E, \text{jump})$ where:

- (1) $X = \{x_1, \dots, x_n\}$ finite set of real valued variables
 Q finite set of control modes
- (2) $\{\text{flow}_q \mid q \in Q\}$ specify the continuous dynamics in each control mode
(flow_q predicate over $\{x_1, \dots, x_n\} \cup \{\dot{x}_1, \dots, \dot{x}_n\}$).
- (3) $\{\text{Inv}_q \mid q \in Q\}$ mode invariants (predicates over X).
- (4) $\{\text{Init}_q \mid q \in Q\}$ initial states for control modes (predicates over X).
- (5) E : control switches (finite multiset with elements in $Q \times Q$).
- (6) $\{\text{guard}_e \mid e \in E\}$ guards for control switches (predicates over X).
- (7) Jump conditions $\{\text{jump}_e \mid e \in E\}$, (predicates over $X \cup X'$), where $X' = \{x'_1, \dots, x'_n\}$ is a copy of X consisting of “primed” variables.

Linear Hybrid Automata

Atomic linear predicate: linear inequality (e.g. $3x_1 - x_2 + 7x_5 \leq 4$).

Convex linear predicate: finite conjunction of linear inequalities.

A state assertion s for S : family $\{s(q) \mid q \in Q\}$, where $s(q)$ is a predicate over X (expressing constraints which hold in state s for mode q).

Definition [Henzinger 1997] A linear hybrid automaton (LHA) is a hybrid automaton which satisfies the following requirements:

(1) **Linearity:**

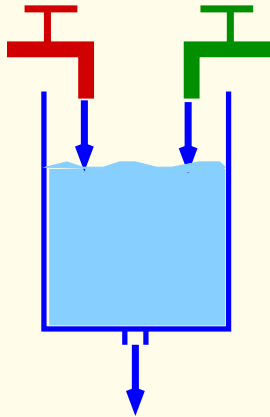
- For every $q \in Q$, flow_q , Inv_q , and Init_q are convex linear predicates.
- For every $e = (q, q') \in E$, jump_e and guard_e are convex linear predicates.

We assume that flow_q are conjunctions of *non-strict* inequalities.

(2) **Flow independence:**

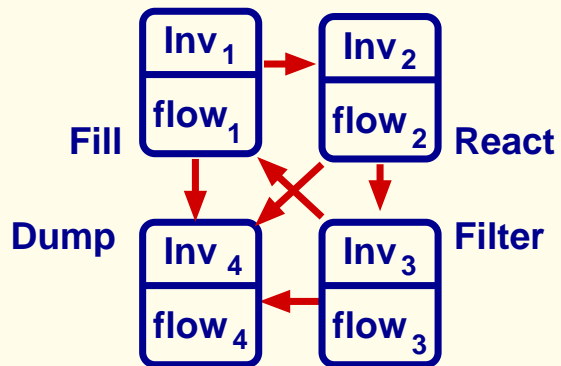
For every $q \in Q$, flow_q is a predicate over \dot{X} only.

Example

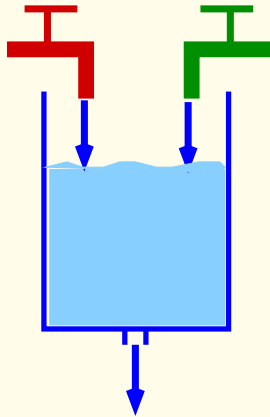


Chemical plant

Two substances are mixed; they react; the resulting product is filtered out; then the procedure is repeated.



Example

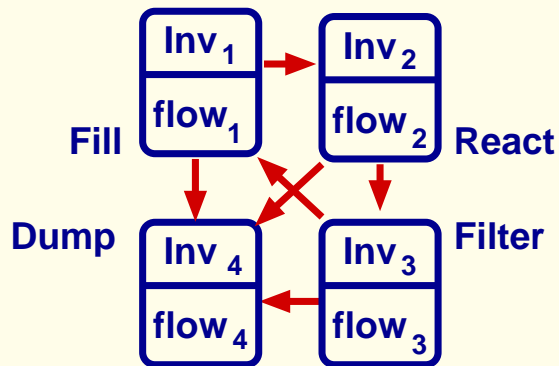


Chemical plant

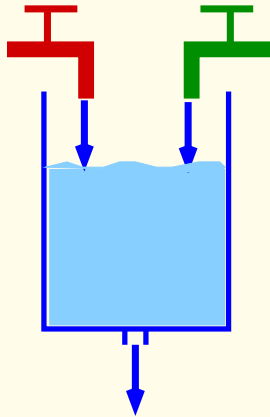
Two substances are mixed; they react; the resulting product is filtered out; then the procedure is repeated.

Check:

- No overflow
- Substances in the right proportion
- If substances in wrong proportion, tank can be drained in ≤ 200 s.



Example

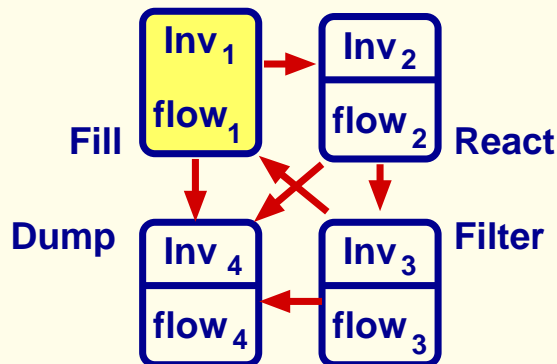


Mode 1: Fill Temperature is low, 1 and 2 do not react.

Substances 1 and 2 (possibly mixed with a small quantity of 3) are filled in the tank in equal quantities up to a margin of error.

$$\text{Inv}_1 \quad x_1 + x_2 + x_3 \leq L_f \wedge \bigwedge_{i=1}^3 x_i \geq 0 \wedge \\ -\epsilon_a \leq x_1 - x_2 \leq \epsilon_a \wedge 0 \leq x_3 \leq \min$$

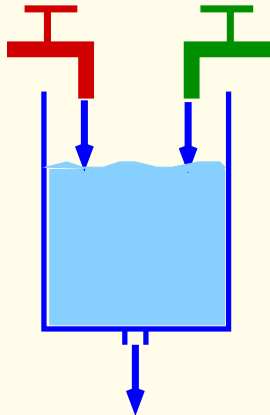
$$\text{flow}_1 \quad \dot{x}_1 \geq \text{dmin} \wedge \dot{x}_2 \geq \text{dmin} \wedge \dot{x}_3 = 0 \wedge -\delta_a \leq \dot{x}_1 - \dot{x}_2 \leq \delta_a$$



If proportion not kept: system jumps into mode 4 (**Dump**);

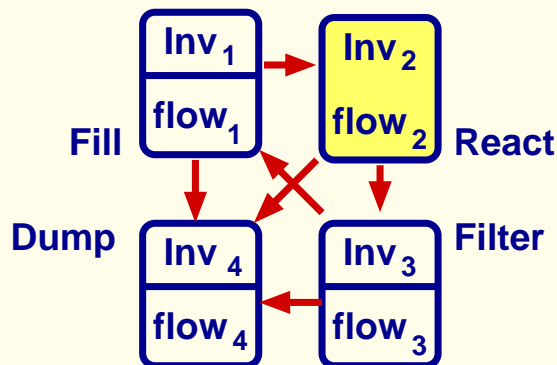
If the total quantity of substances exceeds level L_f (tank filled) the system jumps into mode 2 (**React**).

Example



Mode 2: React Temperature is high. Substances 1 and 2 react. The reaction consumes equal quantities of substances 1 and 2 and produces substance 3.

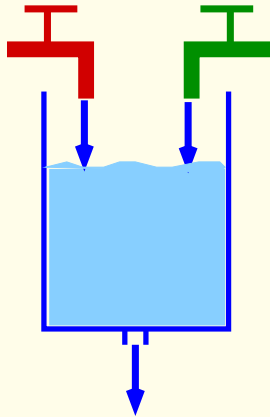
$$\begin{aligned} \text{Inv}_2 \quad & L_f \leq x_1 + x_2 + x_3 \leq L_{\text{overflow}} \wedge \bigwedge_{i=1}^3 x_i \geq 0 \wedge \\ & -\epsilon_a \leq x_1 - x_2 \leq \epsilon_a \wedge 0 \leq x_3 \leq \max \\ \text{flow}_2 \quad & \dot{x}_1 \leq -\text{dmin} \wedge \dot{x}_2 \leq -\text{dmin} \wedge \dot{x}_3 \geq \text{dmin} \\ & \wedge \dot{x}_1 = \dot{x}_2 \wedge \dot{x}_3 + \dot{x}_1 + \dot{x}_2 = 0 \end{aligned}$$



If the proportion between substances 1 and 2 is not kept the system jumps into mode 4 (**Dump**);

If the total quantity of substances 1 and 2 is below some minimal level min the system jumps into mode 3 (**Filter**).

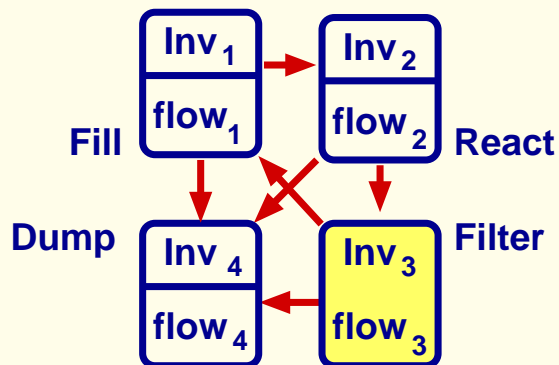
Example



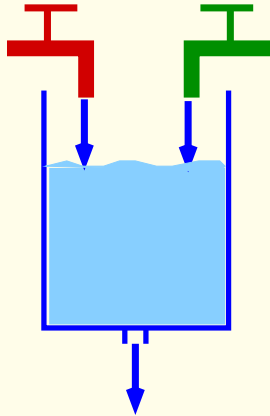
Mode 3: Filter Temperature is low. Substance 3 is filtered out.

$$\begin{aligned} \text{Inv}_3 \quad & x_1 + x_2 + x_3 \leq L_{\text{overflow}} \quad \wedge \quad \bigwedge_{i=1}^3 x_i \geq 0 \quad \wedge \\ & -\epsilon_a \leq x_1 - x_2 \leq \epsilon_a \quad \wedge \quad x_3 \geq \min \\ \text{flow}_3 \quad & \dot{x}_1 = 0 \wedge \dot{x}_2 = 0 \quad \wedge \quad \dot{x}_3 \leq -d_{\min} \end{aligned}$$

If proportion not kept: system jumps into mode 4 (**Dump**);
Otherwise, if the concentration of substance 3 is below some minimal level min the system jumps into mode 1 (**Fill**).



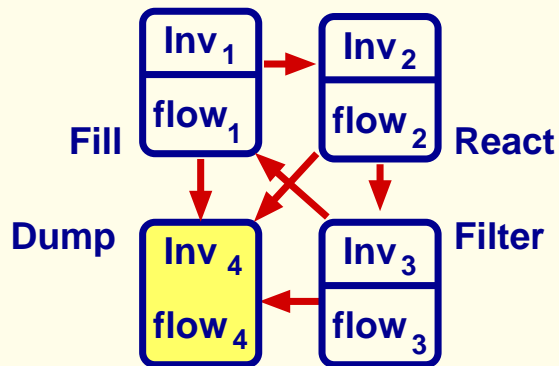
Example



Mode 4: Dump The content of the tank is emptied.

For simplicity we assume that this happens instantaneously:

$$\text{Inv}_4 : \bigwedge_{i=1}^3 x_i = 0 \text{ and } \text{flow}_4 : \bigwedge_{i=1}^3 \dot{x}_i = 0.$$



Remark

The material on ASMs is not required for the exam (only the general idea)

The definitions of timed automata and hybrid automata are required for the exam.

More complex specifications and specification languages

- Languages for describing various processes
- Languages based on Set theory (OZ, B)
- Languages for describing durations
- Complex languages

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CSP

Communicating Sequential Processes, or CSP, is a language for describing processes and patterns of interaction between them.

It is supported by an elegant, mathematical theory, a set of proof tools, and an extensive literature.

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It is supported by an elegant, mathematical theory, a set of proof tools, and an extensive literature.

- Each process: transition system
- Operations on processes: sequential, parallel composition
effects on states

CSP

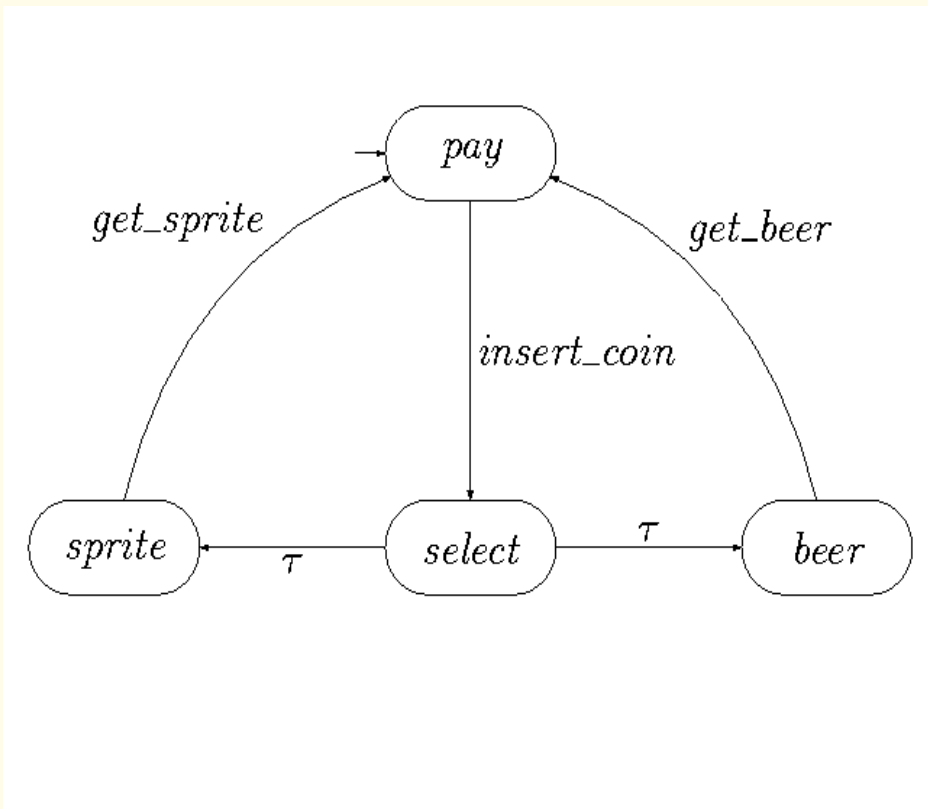
General idea:

Given:

- Set of event names
- Process: behaviour pattern of an object (insofar as it can be described in terms of the limited set of events selected as its alphabet)

CSP

Example:



Events: insert-coin, get-sprite, get-beer

CSP

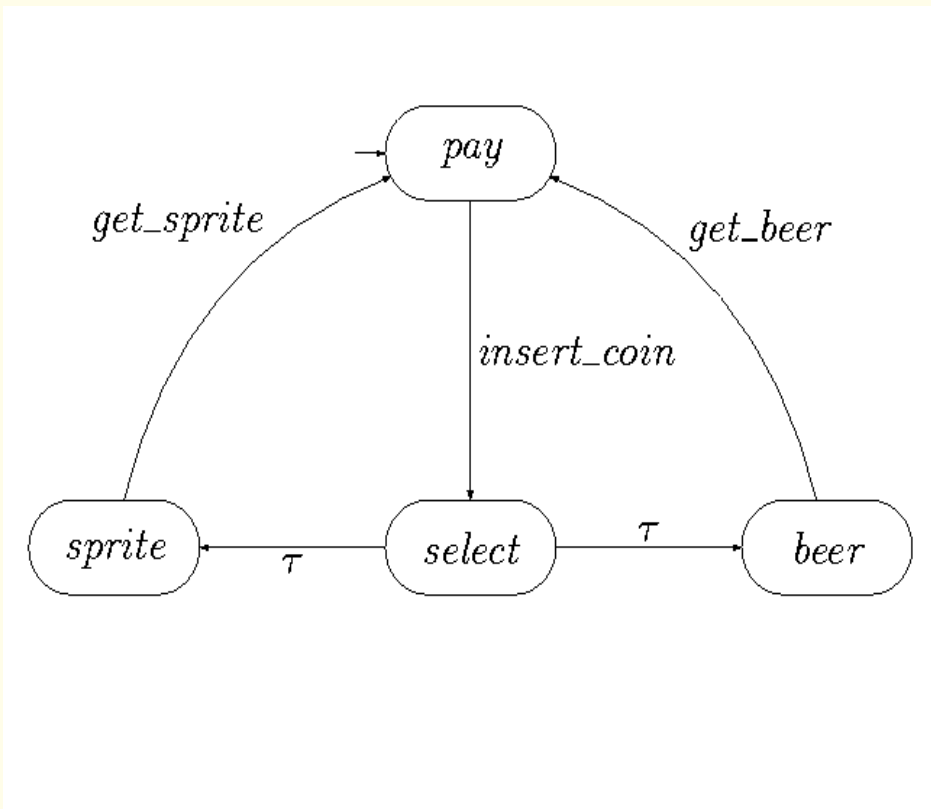
Prefix:

$$P = a \rightarrow Q \qquad (a \text{ then } Q)$$

where a is an event and Q a process

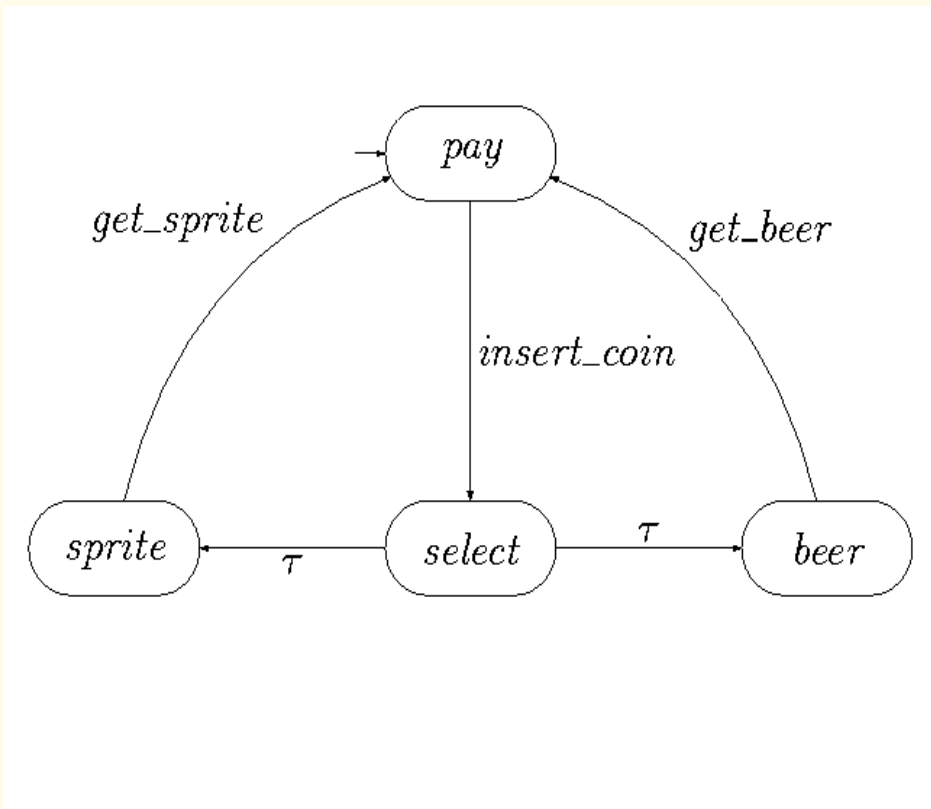
After event a , process P behaves like process Q

CSP: Example



A simple vending machine which consumes one coin before breaking
(*insert-coin* \rightarrow *STOP*)

CSP: Example



A simple vending machine that successfully serves two customers before breaking

$(insert_coin \rightarrow (get_sprite \rightarrow (insert_coin \rightarrow (get_beer \rightarrow STOP))))$

CSP

Example: (recursive definitions)

Consider the simplest possible everlasting object, a clock which never does anything but tick (the act of winding is deliberately ignored)

$$Events(CLOCK) = \{tick\}$$

Consider next an object that behaves exactly like the clock, except that it first emits a single tick

$$(tick \rightarrow CLOCK)$$

The behaviour of this object is indistinguishable from that of the original clock. This reasoning leads to formulation of the equation

$$CLOCK = (tick \rightarrow CLOCK)$$

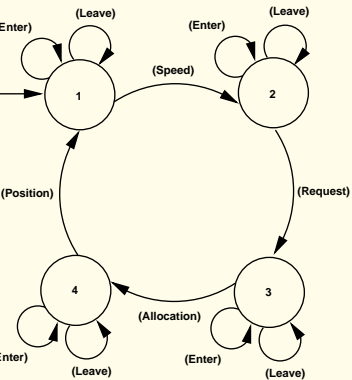
This can be regarded as an implicit definition of the behaviour of the clock.

Modular Specifications: CSP-OZ-DC (COD)

COD [Hoenicke,Olderog'02] allows us to specify in a modular way:

- the control flow of a system
using Communicating Sequential Processes (CSP)
- the state space and its change
using Object-Z (OZ)
- (dense) real-time constraints over durations of events
using the Duration Calculus (DC)

Example: Controller for line track (RBC)



RBC

```
method enter : [s1? : Segment; t0? : Train; t1? : Train; t2? : Train]
method leave : [ls? : Segment; lt? : Train]
local_chan alloc, req, updPos, updSpd
```

```
main  $\sqsubseteq$  ((enter  $\rightarrow$  main)
 $\square$  (leave  $\rightarrow$  main)
 $\square$  (updSpd  $\rightarrow$  State1))
State1  $\sqsubseteq$  ((enter  $\rightarrow$  State1)
 $\square$  (leave  $\rightarrow$  State1)
 $\square$  (req  $\rightarrow$  State2))
```

SegmentData

```
train : Segment  $\rightarrow$  Train [Train on segment]
req : Segment  $\rightarrow$   $\mathbb{Z}$  [Requested by train]
alloc : Segment  $\rightarrow$   $\mathbb{Z}$  [Allocated by train]
```

```
State2  $\sqsubseteq$  ((alloc  $\rightarrow$  State3)
 $\square$  (enter  $\rightarrow$  State2)
 $\square$  (leave  $\rightarrow$  State2))
State3  $\sqsubseteq$  ((enter  $\rightarrow$  State3)
 $\square$  (leave  $\rightarrow$  State3)
 $\square$  (updPos  $\rightarrow$  main))
```

TrainData

```
segm : Train  $\rightarrow$  Segment [Train segment]
next : Train  $\rightarrow$  Train [Next train]
spd : Train  $\rightarrow$   $\mathbb{R}$  [Speed]
pos : Train  $\rightarrow$   $\mathbb{R}$  [Current position]
prev : Train  $\rightarrow$  Train [Prev. train]
```

Init

```
 $\forall t : \text{Train} \Gamma \text{train}(\text{segm}(t)) = t$ 
 $\forall t : \text{Train} \Gamma \text{next}(\text{prev}(t)) = t$ 
 $\forall t : \text{Train} \Gamma \text{prev}(\text{next}(t)) = t$ 
 $\forall t : \text{Train} \Gamma 0 \leq \text{pos}(t) \leq \text{length}(\text{segm}(t))$ 
 $\forall t : \text{Train} \Gamma 0 \leq \text{spd}(t) \leq \text{lmax}(\text{segm}(t))$ 
 $\forall t : \text{Train} \Gamma \text{alloc}(\text{segm}(t)) = \text{tid}(t)$ 
 $\forall t : \text{Train} \Gamma \text{alloc}(\text{nexts}(\text{segm}(t))) = \text{tid}(t)$ 
 $\vee \text{length}(\text{segm}(t)) - \text{bd}(\text{spd}(t)) > \text{pos}(t)$ 
 $\forall s : \text{Segment} \Gamma \text{segm}(\text{train}(s)) = s$ 
```

sd : SegmentData
td : TrainData

```
 $\forall t : \text{Train} \Gamma \text{tid}(t) > 0$ 
 $\forall t1, t2 : \text{Train} \mid t1 \neq t2 \Gamma \text{tid}(t1) \neq \text{tid}(t2)$ 
 $\forall s : \text{Segment} \Gamma \text{prevs}(\text{nexts}(s)) = s$ 
 $\forall s : \text{Segment} \Gamma \text{nexts}(\text{prevs}(s)) = s$ 
 $\forall s : \text{Segment} \Gamma \text{sid}(s) > 0$ 
 $\forall s : \text{Segment} \Gamma \text{sid}(\text{nexts}(s)) > \text{sid}(s)$ 
 $\forall s1, s2 : \text{Segment} \mid s1 \neq s2 \Gamma \text{sid}(s1) \neq \text{sid}(s2)$ 
 $\forall s : \text{Segment} \mid s \neq \text{snil} \Gamma \text{length}(s) > d + \text{gmax} \cdot \Delta t$ 
 $\forall s : \text{Segment} \mid s \neq \text{snil} \Gamma 0 < \text{lmax}(s) \wedge \text{lmax}(s) \leq \text{gmax}$ 
 $\forall s : \text{Segment} \Gamma \text{lmax}(s) \geq \text{lmax}(\text{prevs}(s)) - \text{decmax} \cdot \Delta t$ 
 $\forall s1, s2 : \text{Segment} \Gamma \text{tid}(\text{incoming}(s1)) \neq \text{tid}(\text{train}(s2))$ 
```

effect_updSpd

$\Delta(\text{spd})$

```
 $\forall t : \text{Train} \mid \text{pos}(t) < \text{length}(\text{segm}(t)) - d \wedge \text{spd}(t) - \text{decmax} \cdot \Delta t > 0$ 
 $\Gamma \text{max}\{0, \text{spd}(t) - \text{decmax} \cdot \Delta t\} \leq \text{spd}'(t) \leq \text{lmax}(\text{segm}(t))$ 
 $\forall t : \text{Train} \mid \text{pos}(t) \geq \text{length}(\text{segm}(t)) - d \wedge \text{alloc}(\text{nexts}(\text{segm}(t))) = \text{tid}(t)$ 
 $\Gamma \text{max}\{0, \text{spd}(t) - \text{decmax} \cdot \Delta t\} \leq \text{spd}'(t) \leq \min\{\text{lmax}(\text{segm}(t)), \text{lmax}(\text{nexts}(\text{segm}(t)))\}$ 
 $\forall t : \text{Train} \mid \text{pos}(t) \geq \text{length}(\text{segm}(t)) - d \wedge \neg \text{alloc}(\text{nexts}(\text{segm}(t))) = \text{tid}(t)$ 
 $\Gamma \text{spd}'(t) = \text{max}\{0, \text{spd}(t) - \text{decmax} \cdot \Delta t\}$ 
```

CSP

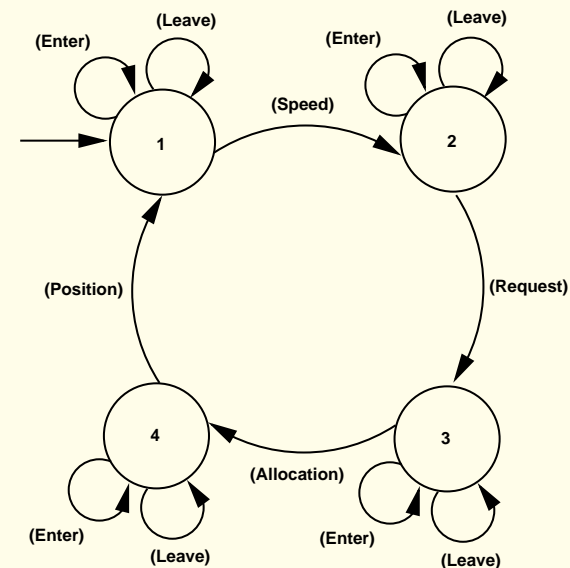
OZ

Example: Controller for line track (RBC)

CSP part: specifies the processes and their interdependency.

The RBC system passes repeatedly through four phases, modeled by events:

- **updSpd** (speed update)
- **req** (request update)
- **alloc** (allocation update)
- **updPos** (position update)



Between these events, trains may leave or enter the track (at specific segments), modeled by the events **leave** and **enter**.

Example: Controller for line track (RBC)

CSP part: specifies the processes and their interdependency.

The RBC system passes repeatedly through four phases, modeled by events with corresponding COD schemata:

CSP: _____

method *enter* : [*s1?* : *Segment*; *t0?* : *Train*; *t1?* : *Train*; *t2?* : *Train*]

method *leave* : [*ls?* : *Segment*; *lt?* : *Train*]

local_chan *alloc*, *req*, *updPos*, *updSpd*

$\text{main} \stackrel{c}{=} ((\text{updSpd} \rightarrow \text{State1}) \quad \text{State1} \stackrel{c}{=} ((\text{req} \rightarrow \text{State2}) \quad \text{State2} \stackrel{c}{=} ((\text{alloc} \rightarrow \text{State3}) \quad \text{State3} \stackrel{c}{=} ((\text{updPos} \rightarrow \text{main})$

$\square(\text{leave} \rightarrow \text{main}) \quad \square(\text{leave} \rightarrow \text{State1}) \quad \square(\text{leave} \rightarrow \text{State2}) \quad \square(\text{leave} \rightarrow \text{State3})$

$\square(\text{enter} \rightarrow \text{main}) \quad \square(\text{enter} \rightarrow \text{State1}) \quad \square(\text{enter} \rightarrow \text{State2}) \quad \square(\text{enter} \rightarrow \text{State3}))$

Example: Controller for line track (RBC)

OZ part. Consists of data classes, axioms, the `Init` schema, update rules.

Example: Controller for line track (RBC)

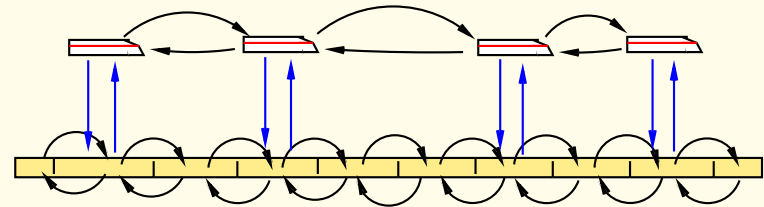
OZ part. Consists of data classes, axioms, the Init schema, update rules.

- 1. **Data classes** declare function symbols that can change their values during runs of the system

Data structures:

- 2-sorted pointers

train: trains
segm: segments



SegmentData

$train : Segment \rightarrow Train$

[Train on segment]

$req : Segment \rightarrow \mathbb{Z}$

[Requested by train]

$alloc : Segment \rightarrow \mathbb{Z}$

[Allocated by train]

TrainData

$segm : Train \rightarrow Segment$

[Train segment]

$next : Train \rightarrow Train$

[Next train]

$spd : Train \rightarrow \mathbb{R}$

[Speed]

$pos : Train \rightarrow \mathbb{R}$

[Current position]

$prev : Train \rightarrow Train$

[Prev. train]

Example: Controller for line track (RBC)

OZ part. Consists of data classes, axioms, the Init schema, update rules.

- **1. Data classes** declare function symbols that can change their values during runs of the system, and are used in the OZ part of the specification.
- **2. Axioms:** define properties of the data structures and system parameters which do not change
 - $gmax : \mathbb{R}$ (the global maximum speed),
 - $decmax : \mathbb{R}$ (the maximum deceleration of trains),
 - $d : \mathbb{R}$ (a safety distance between trains),
 - Properties of the data structures used to model trains/segments

Example: Controller for line track (RBC)

OZ part. Consists of data classes, axioms, the Init schema, update rules.

- **3. Init schema.** describes the initial state of the system.
 - trains - doubly-linked list; placed correctly on the track segments
 - all trains respect their speed limits.
- **4. Update rules** specify updates of the state space executed when the corresponding event from the CSP part is performed.

Example: Speed update

effect_updSpd
 $\Delta(sp d)$

$$\begin{aligned} & \forall t : Train \mid pos(t) < length(seg m(t)) - d \wedge spd(t) - decmax \cdot \Delta t > 0 \\ & \quad \Gamma \max\{0, spd(t) - decmax \cdot \Delta t\} \leq spd'(t) \leq lmax(seg m(t)) \\ & \forall t : Train \mid pos(t) \geq length(seg m(t)) - d \wedge alloc(nexts(seg m(t))) = tid(t) \\ & \quad \Gamma \max\{0, spd(t) - decmax \cdot \Delta t\} \leq spd'(t) \leq \min\{lmax(seg m(t)), lmax(nexts(seg m(t)))\} \\ & \forall t : Train \mid pos(t) \geq length(seg m(t)) - d \wedge \neg alloc(nexts(seg m(t))) = tid(t) \\ & \quad \Gamma spd'(t) = \max\{0, spd(t) - decmax \cdot \Delta t\} \end{aligned}$$

Formal specification

- Specification for program/system
- Specification for properties of program/system

Verification tasks:

Check that the specification of the program/system has the required properties.